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# Pltw Digital Electronics Final Exam

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DE Part A Answer Key Spring - Mrs-o-c

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DE Final Exam (201112) A PLTW Digital Electronics Saved As: DE - Final Exam (201112) A 5

21.)The following waveform patter with A and

B are inputs, X is the output, is for a(n) a. 2input AND gate b. 2-

input OR gate c. 2input XOR gate d. 2input XNOR gate e. 2-

input NAND gate 22.)Use the grid to match the following Boolean expr

essions to the switch circuit that best describes its function.

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A Sample Exam for Digital Electronics By Patrick Hoppe.

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Students answer 10 questions in a sample exam. This exercise covers number systems, number conversion, logic gates, and combinational design. Feedback is provided.

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Exam (201112) A 4 Determine the color code of the resistor with the following value. 16.)230 K  $\pm 10\%$

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Quia - Digital Electronics Final Exam

Digital Electronics (DE), one of the

specialized courses in TCD's PLTW Pathway to Engineering course sequence. This is a high

school level course for students interested

in electronics. This course assumes no

previous knowledge, but students are Page

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Digital Electronics Final Examination Part A

Multiple Choice ANSWER KEY Spring 2009 PRACTICE

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**Max. Clock Freq. in Digital Circuits Finding and**  
**Eliminating Hazards in the circuit using K-Map**  
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Face Filter Logic Families | Logic Family - 1 | Lec  
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**Lec 40 | Digital Electronics | GATE EE \u0026 ECE**  
**Exam Combinational Circuits | Hazards | Lec 27 |**  
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**Data Converters | DAC - 2 | Lec 48 | Digital**  
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